Korenix Jetl/O 6520 Industrial Intelligent Ethernet I/O Server

User Manual

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Korenix Jetl/O 6520 Industrial Intelligent Ethernet I/O Server User Manual

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Index

1	INTR	ODUCTION	1
	1.1	OVERVIEW OF JETI/O 6500 SERIES	1
	1.2	PACKAGE CHECKLIST	2
	1.3	JETI/O 6520 INTRODUCTION	2
	1.4	JETI/O 6520 PRODUCT SPECIFICATION	3
2	HAR	DWARE INSTALLATION	4
	2.1	Hardware Introduction	4
	2.2	WIRING POWER INPUT	5
	2.3	WIRING I/O CONNECTORS	6
	2.4	JETI/O 6520 WIRING EXAMPLE	6
	2.5	WIRING EARTH GROUND	7
	2.6	WIRING FAST ETHERNET PORTS	7
	2.7	DIN-RAIL MOUNTING INSTALLATION	7
3	PRE	PARATION FOR MANAGEMENT	9
	3.1	Understand the Ethernet I/O Server Architecture	9
	3.2	PREPARATION FOR REMOTE MANAGEMENT	9
4	FEAT	TURE CONFIGURATION	.11
	4.1	BLOCK I/O CONFIGURATION UTILITY	. 11
	4.2	BLOCK I/O OPC SERVER UTILITY	28
	4.3	SNMP	32
	4.4	WEB UI	32
	4.5	How to Upgrade Firmware	32
5	MOD	BUS/TCP COMMAND SET	.37
	5.1	INTRODUCTION OF MODBUS/TCP PROTOCOL	.37
	5.2	JETI/O 6520 MODBUS/TCP ADDRESS MAPPING	38
6	APP	ENDIX	76
	6.1	SNMP MIB	.76
	6.2	ANALOG OUTPUT (DA) SLEW RATE AND CALIBRATION	.77
	63	REVISION HISTORY	70

1 Introduction

Welcome to Korenix *Jetl/O 6500* Series Industrial Managed Ethernet I/O Module User Manual. Following topics are covered in this chapter:

- 1.1 Overview of Jetl/O 6500 Series
- 1.2 Package Checklist
- 1.3 Jetl/O 6520 Introduction
- 1.4 Jetl/O 6520 Product Specification

1.1 Overview of Jetl/O 6500 Series

Jetl/O 6500 series is a series of Intelligent Ethernet I/O server for distributive monitoring and controls. The JeI/O 6500 series equipped with one Ethernet port and multiple channels Analog Input/Output, Digital Input/Output and temperature measurement connectors. Thus users can easily perform I/O data collecting, status changing, automatically activate events… through the Ethernet network. JetI/O 6500 series provides Windows Utilities, and SNMP for configuration. And support Modbus/TCP protocol, OPC Server for Modbus/TCP, thus user can easily monitor and control the remote I/O devices and combine the JetI/O with existed HMI/SCADA package.

Naming Rule: Jetl/O 65AB

A: Major Feature

- 1: Analog Input Series. Includes the RTD input, Thermocouple Input
- 2: Analog Output Series
- 3: Digital Input Series
- 4: Digital Output Series
- 5: Digital Input and Digital Output Series

B: Sequence Number

Jetl/O 6500 Series includes:

Jetl/O 6510: Industrial Intelligent 8-CH Analog Input Ethernet I/O Server

Jetl/O 6511: Industrial Intelligent 8-CH Thermocouple Input Ethernet I/O Server

Jetl/O 6512: Industrial Intelligent 4-CH RTD Input Ethernet I/O Server

Jetl/O 6520: Industrial Intelligent 4-CH Analog Output Ethernet I/O Server

Jetl/O 6550: Industrial Intelligent 14-CH DI and 8-CH DO Ethernet I/O Server

1.2 Package Checklist

Korenix Jetl/O 6500 Series products are shipped with following items:

- One Ethernet I/O Module
- One attached DIN-Rail clip
- Terminal Blocks for I/O and Power Input
- Documentation and Software CD
- Quick Installation Guide

If any of the above items are missing or damaged, please contact your local sales representative.

1.3 Jetl/O 6520 Introduction

JetI/O 6520 is an intelligent I/O Server equipped with one Ethernet port and 4 channels Analog Output. Jet I/O 6520 provides 12 bits resolution and high accuracy for voltage/current output. Each AO channel equipped with the isolation of 2500Vms to protect your I/O server from damage, independent output operation and 0.125 to 128 programmable output slew rate are the best choice for analog output application.

JetI/O 6520 provides Windows Utilities, SNMP for configuration. Industrial Modbus/TCP protocol and OPC Server driver for integrating JetI/O with existed HMI/SCADA. Robust aluminum case with good heat dispersing and IP31 protection. With JetI/O users can easily perform status monitoring and control the remote I/O devices.

1.4 Jetl/O 6520 Product Specification

System

CPU: 100MHZ, RISC-Based

SDRAM: 32K bytes

Flash ROM: 512K bytes EEPROM: 256 bytes

Watchdog Timer: 1.0 sec H/W

LED:

PWR: Power Input plugged and On (Red)

RDY: System startup ready (Green)

Network Interface

Ethernet: IEEE 802.3 10Base-T

IEEE 802.3u 100Base-TX

Connector: 1 * RJ-45, Auto MDI/MDI-X

Protection: Built-in 1.5 KV magnetic isolation

protection

LED:

Upper (LAN Activity): Orange ON & Blinking

Lower (10M/100M): 10M(Green OFF), 100M (Green

ON)

PWR: Power On (Green)

RDY: System boot up Ready (Red), system booting (N $\,$

LED)

Network Protocols: IP, TCP, UDP, SNMP, HTTP,

Telnet, BOOTP, DHCP

Analog Output

Output Channels: 4 Channels, differential

Resolution: 12 bits

Voltage Output Range: 0 ~ 10V, -10V ~ +10V

Current Drive: +/- 5mA

Current Output Range: Sink Type, 0-20mA

Excitation Voltage: 12-36V

Programmable Output Slope to Safety Value:

0.125 to 128 mA/sec (Current)

0.0625 to 64 V/sec (Voltage)

Accuracy: ±0.1% of FSR ±1LSB Output Impedance: 0.5 ohm

Current Load Resistor: 0 to 500 ohm

Isolation Voltage: 2500Vrms

Feature

Network Protocols: IP, TCP, UDP, SNMP, HTTP,

BOOTP, DHCP, Modbus/TCP, OPC Server

 $\textbf{Configuration:} \ \textbf{Windows} \ \textbf{Utility}, \ \textbf{SNMP}, \ \textbf{DHCP} \ \textbf{Client},$

TFTP Server for firmware update Windows Utility: Block I/O Utility

OPC Server Utility: OPC Server for Modbus/TCP

SNMP: MIB-II: System, SNMP Trap and Private MIB

SNMP Trap Server: Up to 4 SNMP Trap Server

Power Requirements

System Power: external unregulated +24V (18-32V)

Power Consumption: Max. 3 W

Mechanical

Dimensions: 120 (H) x 55 (W) x 75 (D)mm

Mounting: Din-Rail

Material: Aluminum

Environmental

Regulatory Approvals: CE, FCC Class A

Operating Temperature: $-25 \sim 70$ °C

Operating Humidity: 0 ~ 95% non-condensing

Storage Temperature: -40 ~ 80°C

Warranty: 3 years

2 Hardware Installation

This chapter includes hardware introduction, installation and configuration information.

Following topics are covered in this chapter:

2.1 Hardware Introduction

Dimension

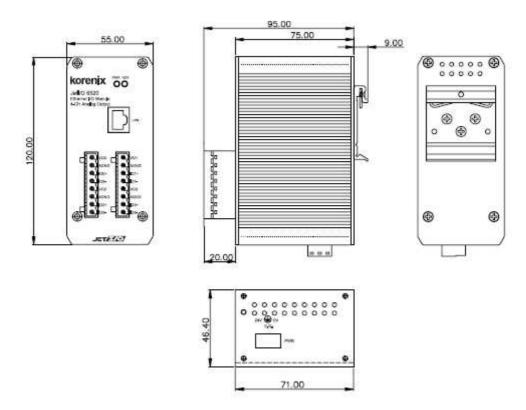
Appearance

LED Indicators

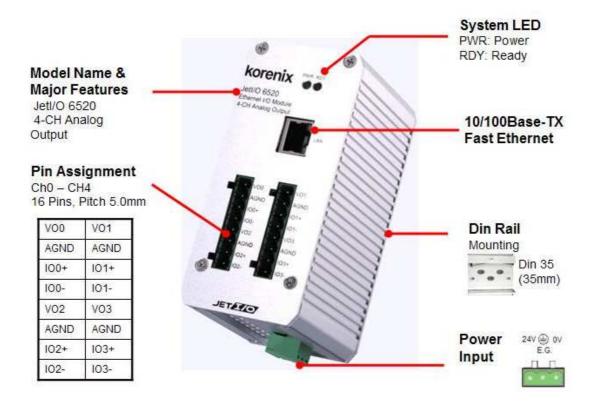
- 2.2 Wiring Power Input
- 2.3 Wiring I/O Connectors
- 2.4 Wiring Ethernet Ports
- 2.5 DIN-Rail Mounting Installation

2.1 Hardware Introduction

Dimensions: 120 (H) x 55 (W) x 75 (D) mm



Jetl/O 6520 Appearance:



LED Indicators:

System LED		
PWR	Power Input plugged and On (Green)	
RDY	System startup ready (Red)	
Ethernet LED		
Upper (LAN Activity)	Orange On & Blinking	
Lower(10M/100M)	10M (Green Off) /100M(Green ON	

2.2 Wiring Power Input

Follow below steps to wire Jetl/O DC power inputs.

- 1. Follow the pin assignment to insert the wires into the contacts on the terminal block connector.
- 2. Tighten the wire-clamp screws to prevent DC wires from being loosened.
- 3. Connect to and turn on the power source. The suitable working voltage is 24VDC.
- 4. When the unit is ready, the PWR LED turns Greed, the RDY LED turns Red.

Note1: It is a good practice to turn off input and load power, and to unplug power terminal block before making wire connections. Otherwise, your screwdriver blade can inadvertently short your terminal connections to the grounded enclosure.

Note 2: The range of the suitable electric wire is from 12 to 24 AWG.

2.3 Wiring I/O Connectors

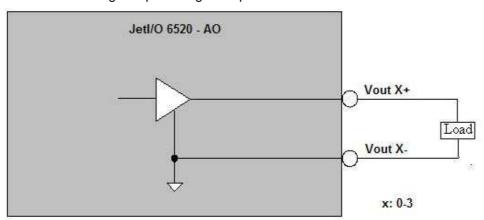
Follow the pin assignment to insert the wires into the front contacts on the terminal block connector. Tighten the wire-clamp screws to prevent the I/O wires from being loosened.

The wiring diagram of the Jetl/O 6520 is as below:

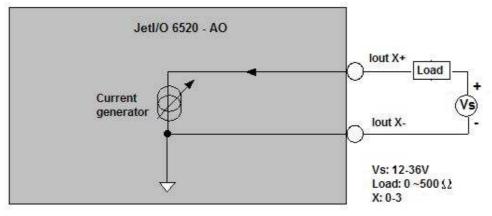
Pin No	Description	Pin No	Description
VO0	CH0 Voltage Output	VO1	CH1 Voltage Output
AGND	Analog GND	AGND	Analog GND
IO0+	CH0 Current Output +	IO1+	CH1 Current Output +
IO0-	CH0 Current Output -	IO1-	CH1 Current Output -
VO2	CH2 Voltage Output	VO3	CH3 Voltage Output
AGND	Analog GND	AGND	Analog GND
IO2+	CH2 Current Output +	IO3+	CH3 Current Output +
IO2-	CH2 Current Output -	IO3-	CH3 Current Output -

2.4 Jetl/O 6520 Wiring Example

2.4.1 Jetl/O 6520 Voltage output wiring example



2.4.2 Jetl/O 6520 Current output wiring example



Note: There are 4 pins for each channel, 2 pins for Voltage output, and 2 pins for Current

output. Only one of the output type, voltage or current can be used for one channel. Each channel can run Voltage or Current output independently.

2.5 Wiring Earth Ground

To ensure the system will not be damaged by noise or any electrical shock, we suggest you to make exact connection with Jetl/O products with Earth Ground.

On the bottom side of Jetl/O 6500 Series, there is one power earth ground pin in the Power Input terminal block.

Pin No	Description
1(+24V)	DC+24V Power Input
2(FGND)	Power Earth Ground
3(0V)	Referenced Ground for Power Input

2.6 Wiring Fast Ethernet Ports

Jetl/O 6500 series includes 1 RJ45 Fast Ethernet ports. The fast Ethernet ports support 10Base-T and 100Base-TX, full or half duplex modes. The fast Ethernet port will auto-detect the signal from connected devices to negotiate the link speed and duplex mode. Auto MDI/MDIX allows users to connect another switch, hub or workstation without changing straight through or crossover cables.

Connect one side of an Ethernet cable into the Ethernet port and connect the other side to the attached switch or host. The link LED will light up when the cable is correctly connected. Refer to the **LED Indicators** section for descriptions of each LED indicator. Always make sure that the cables length between the 2 ends is less than 100 meters (328 feet).

The wiring cable types are as below.

10Base-T: 2-pair UTP/STP Cat. 3, 4, 5 cable, EIA/TIA-568 100-ohm (100m) 100 Base-TX: 2-pair UTP/STP Cat. 5 cable, EIA/TIA-568 100-ohm (100m) 1000 Base-TX: 4-pair UTP/STP Cat. 5 cable, EIA/TIA-568 100-ohm (100m)

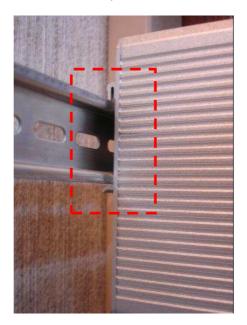
2.7 Din-Rail Mounting Installation

The DIN-Rail clip is already attached to the Jetl/O 6500 Series when packaged. If the DIN-Rail clip is not screwed on the Jetl/O, follow the instructions and the figure below to attach DIN-Rail clip to Jetl/O.

a. Insert the upper end of DIN-Rail clip into the back of DIN-Rail track from its upper side.



b. Lightly push the bottom of DIN-Rail clip into the track.



- c. Check if DIN-Rail clip is tightly attached on the track.
- d. Korenix suggests reserve at least 5mm interval distance between the Jetl/O devices. This is good for heat dispersing.
- e. To remove Jetl/O 6500 from the track, reverse the steps above.

3 Preparation for Management

Before you start to configure the Jetl/O, you need to know the system architecture of the Jetl/O products, configure the device's IP address, and then you can remotely manage the Ethernet I/O via the network. This chapter introduces the basic knowledge of the related technologies.

Following topics are covered in this chapter:

- 3.1 Understand the Intelligent Ethernet I/O Server Architecture
- 3.2 Preparation for Remote Management

3.1 Understand the Ethernet I/O Server Architecture

The Figure 1 shows the Jetl/O Intelligent Ethernet I/O Server Architectures. In the top level shows the typical applications run in the remote I/O environment. The middle level is the Ethernet infrastructure. The low level, gray block include the software agent, signal types of the Jetl/O 6500 series intelligent Ethernet I/O Server.

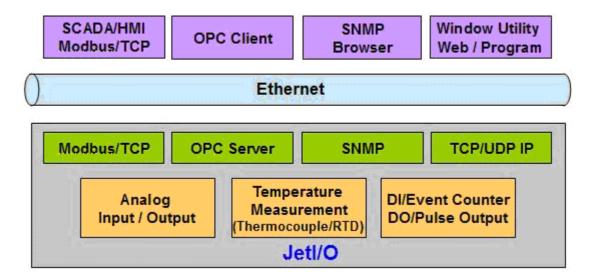


Figure 1. The Jetl/O Intelligent Ethernet I/O Server Architectures.

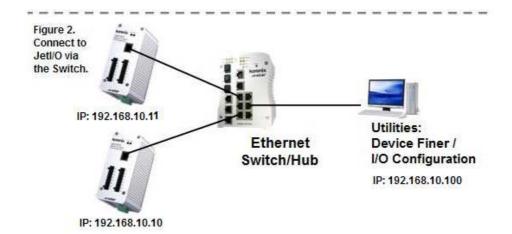
3.2 Preparation for Remote Management

Jetl/O 6500 series Intelligent I/O Server provides several types remote management methods. You can configure the Jetl/O via the Ethernet network. You just need to know the device's IP address and then you can remotely control or monitor the I/O channels' information.

Jetl/O provides several ways for users to configure the IP address. The default IP address is 192.168.10.3. You can directly connect the Jetl/O one after one to change its

IP address. Or connect the Jetl/Os to the same switch or network, then the host PC can modify the IP address via the switch or network.





If you purchase several Jetl/Os and connect them to the same network before change their IP address. They must have the same default IP address, and you may not control them well due to the IP conflict. At this time, you should change their IP address first. The Jetl/O' Block I/O configuration utility and its Device Finder Popup Window can help you to do this.

Note 1: Device Finder Popup Window allows you to discover the Jetl/Os which have the same IP address. Change the IP address of the Jetl/O one after one. After you configured the new IP address for the unit, please notice whether the ARP table of the device is flashed or not. If not, you can choose "Start -> Run", type "cmd" to open the DOS prompt. Use "arp -d" to clear the ARP cache.

Note 2: After changed IP address or changed the DHCP client mode in Block I/O Configuration utility, the utility will automatically reboot the unit. Please rescan the devices after around 5 seconds.

Note 3: You can find the detail progress in the next chapters.

4 Feature Configuration

Jetl/O 6500 series Industrial Managed Ethernet I/O module provides several configuration methods. This chapter introduces the configuration steps.

Following topics are covered in this chapter:

- 4.1 Block I/O Configuration Utility
- 4.2 Block I/O OPC Server Utility
- **4.3 SNMP**
- 4.4 Web UI
- 4.5 Modbus/TCP Command set
 - 4.5.1 Introduction of Modbus/TCP protocol
 - 4.5.2 Jetl/O 6520 Modbus/TCP command set

4.1 Block I/O Configuration Utility

Block I/O Utility is the major Jetl/O Configuration Utility. With this tool, you can browse the available units, view the status of each channel, configure the I/O settings, configure active alarms and conditions&Go logic rule.

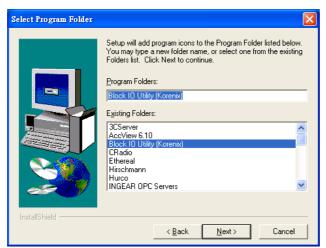
4.1.1 Installation

1. Go to the "Utility -> IO Configuration" folder. Click "Setup.exe" to run the setup progress.



- 2. Click "Next" and type the Name and Company in the "User Information" window. Then click "Next".
- 3. Choose the Destination Directory in the "Choose Installation Location" window. Then click "Next".

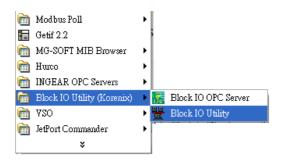
4. Type the name for the Block I/O Configuration Utility or use the default name, Block IO Utility (Korenix) for the program in the "Program Folder" field of the "Select Program Folder" window. Then click "Next".



- 5. Click "Next" in the "Starting Copying File" window to continue the setup progress.
- 6. As long as you see the "Setup Complete" window that means the progress is finished. Click "Finish" to exit the setup progress.



7. Go to "Start" -> "Program", and then you can see the "Block IO Utility (Korenix) folder. There are 2 utilities are installed, Block IO OPC Server and Block IO Utility.

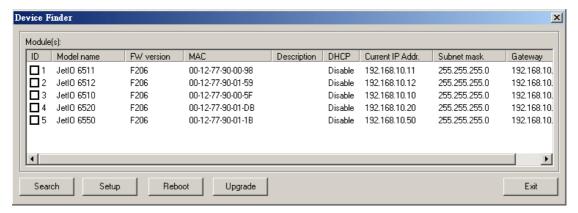


4.1.2 Device Finder

Device Finder helps you search JetlO 6500 devices on the same physical subnet, even if their IP addresses are conflit or if their IP address setting are not on the same subnet with your host PC. Device Finder also helps you configure the IP address and upgrade firmware of the found devices.

Select the Device Finder icon from toolbar or select "Tools"→"Device Finder" to launch Device Finder.

1. Click "Search" to search the Jetl/O devices. You can see the available devices in the list.

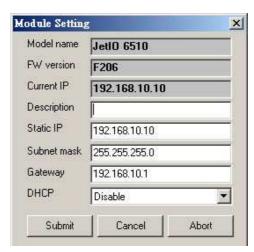


The following information is displayed:

Item	Description
ID	index of the list
Model Name	JetIO model name
FW version	The firmware version
MAC	MAC address
Description	A short description of the device, max 16 characters
DHCP	DHCP client function status: Enable or disable
Current IP Addr	If DHCP is enabled, dynamic IP is acquired from the DHCP server, Else, static IP is assigned as dynamic IP.
Subnet mask	Subnet mask
Gateway	Gateway IP address

2. Select the target unit and click "Setup" button to configure the device. Click "Submit" to apply the new setting.

Note: When changing IP address, the new IP address and the NIC's (Network Interface Card) IP address should be located within the same subnet.



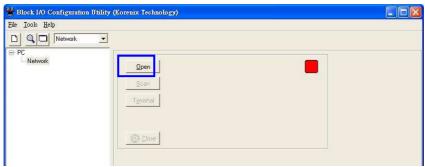
- 3. Select the target units and click "Reboot" to reboot the device. You can reboot one or multiple units in one time.
- 4. Select the target units and click "Upgrade" to upload the new firmware. Please refer to the section 4.4 to know the detail step by step progress.
- 5. Click "Exit" to exit the device finder tool.

Note: Clear the ARP cache (arp –d in DOS prompt) if you can't change the second unit's IP address. In DOS prompt, "arp –a" can help you to see the ARP table. "arp –d" can help you to clear all the ARPs in your host PC.

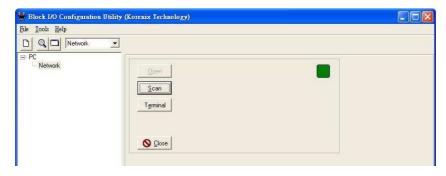


4.1.3 Device Scan

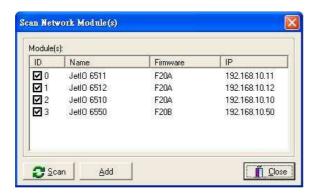
1. Lunch the Block IO Utility and then press "Open" to enable the network Interface.



The square LED shows "Green" after you opened the interface. Click "Close" can close the network interface.

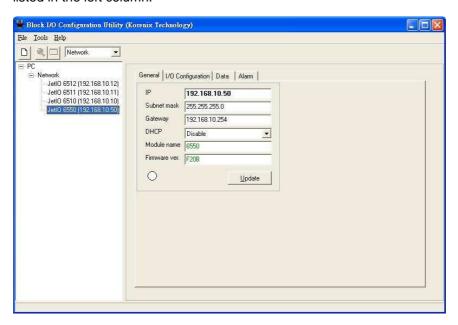


2. Click "Scan" to open the "Scan Network Module(s)" popup window. Click Scan to start the searching.



Note: Please modify the IP address of your target devices. The scan feature can't browse the devices which have the same IP address. Only one of the devices which have the same IP address can be found. This is the current restriction. Please modify the IP address first. You can use Block I/O Utility or Device Finder to do the IP modification.

3. Click "Add" to add the available Jetl/O units. Then you can see the Jetl/O units are listed in the left column.

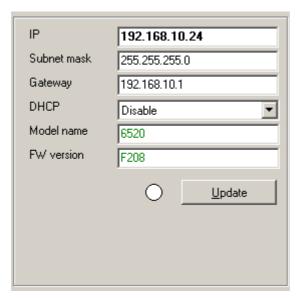


4. Move the mouse over to one of the Jetl/O units. Select the unit then you can configure and monitor the configurations of the Jetl/O. The features Block I/O Configuration utility provides are similar. Please find your model name and go to its configuration introduction chapter in below.

4.1.4 Jetl/O 6520 Configuration

4.1.4.1 Go to "General" page.

Once you select one of the existing I/O modules in the Network interface tree, the main window defaults to the "General" page.



It should be noticed that the privilege setting is required to gain access to do the further configuration options. When you install the I/O module in first time, the default password is "admin". You just need to click on "Login" button and type the password of "Admin" on "password for entry" field. If you want to make password changes, click "Change" button and then the dialog of "Chang Password" prompts you to update the new password with up to six characters.

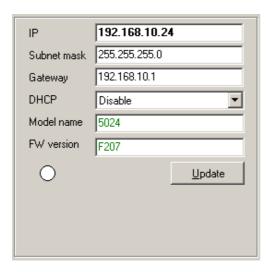


Note 1: The password protection is the new feature provided in firmware F206 and Utility V1.3

Note 2: When you upgrade firmware from F204 to F206, the default password is disabled, please change new password for your device.

Note 3: If you forget the password, you may need to reset the module via 'Reset to default' command, referred to section 2.2.6, to clear the password and load factory defaults. This will result in the clear all the configuration settings as you assigned.

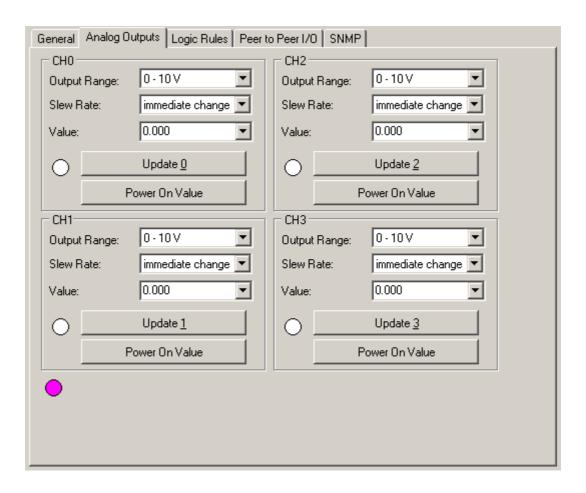
Each module has its own page to display and configure the TCP/IP parameters. If the function of DHCP is disabled, user can type the IP address, Gateway and Subnet Mask information on the page. When everything is ok, user can push the "Update" button. This setting will be affected after restarting the module.



Note: After changed IP address or changed the DHCP client mode, the utility will automatically reboot the unit. Please rescan the devices after around 5 seconds.

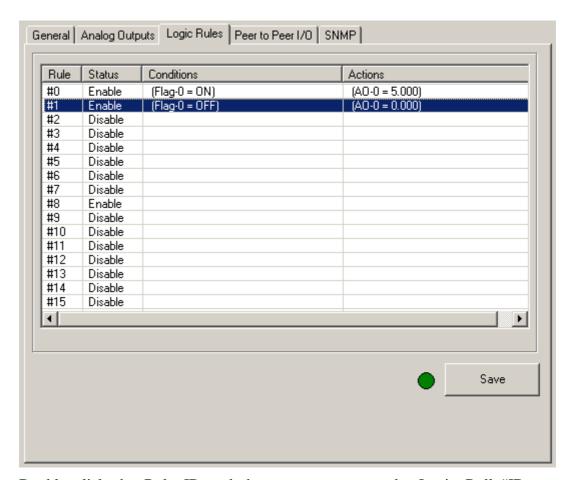
4.1.4.2 Go to "Analog Output" page.

You can choose the output range, choose or manually configure the output value for each channel. Click "**Update 0/1/2/3**" and then the new setting is activated. The value will not be saved after reboot the device. If you want permanently save the output value for next restart, click "**Power On Value**". The round LED shows green when the new setting is correct, show red when the new setting is incorrect.

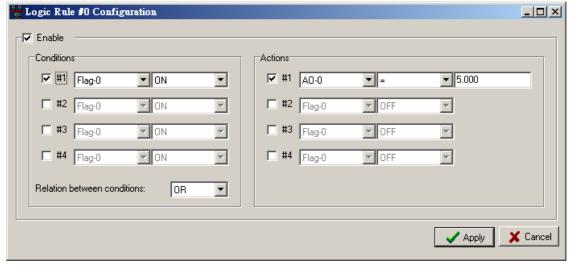


4.1.4.3 Go to "Logic Rules" page.

In this page, you can configure the I/O logic rule. It allows you to define the logic operation and process rules in this utility and then download the rules to the I/O module. The module will automatically execute the logic rules to process different action depending on the input conditions as you defined. The theory is the same as the "IF-Then" rule. It's easy to understand rules, no need know extra program script to configure this.



Double click the Rule ID and then you can go to the Logic Rulle#ID Configuration page. Select "Enable" and configure the Condition and Actions then press "Apply" to enable the rule.



Thus, when the "Conditions" is reached, the system automatically activates the "Actives". For example:

Rule	Status	Condition	Action
#1	Enable	Flag-0 = ON	(AO-0 = 5.0)
#2	Enable	Flag-0 = OFF	(AO-0 = 0.0)

Rule #1: If condition is equal to "Flag-0=ON", the "AO-0" is automatically set to 5V. Again, configure the reverse way in the rule #2: If condition is equal to "Flag-0=ON", the "AO-0" is automatically set to 0V. In this meantime, the local internal flag, that says Flag-0, on the module can be mapping from remote digital inputs on different destination modules via. peer-to-peer I/O activity.

The maximum I/O logic rules support up to 16. Each rule can support up to 4 different conditions and 4 different actions. There are 16 internal flags (i.e., auxiliary channels) on each module. The data type of internal flags is digital, meaning its value is either logic True or logic False. It allows you to choose these internal flags as input for condition rule as well as these flags as output for action state. Therefore, you could easy to use these internal flags to implement logic rule in cascade mode and mirror these flags to/from the remote modules via peer-to-peer I/O activity.

The supported conditions and actions are as below:

Condition		Action	
Internal Flag	ON, OFF, ON to OFF, OFF to ON	Internal Flag	ON, OFF
		Analog Output	= (Number)
		SNMP	(Trap Server IP)
Relation between Conditions	OR, AND		

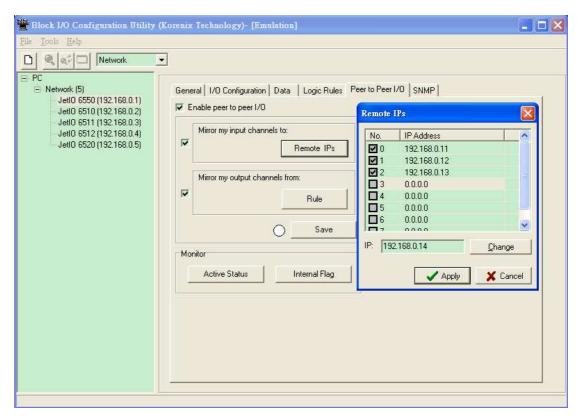
After completing all configurations for I/O logic rule, click "Save" button in the bottom of the Logic Rules area. All the mapping configurations will be flushed into flash memory on the module. In order to extend the flash memory life, it is strongly recommended that you should save all configurations together at one time instead of saving individual setting many times.

4.1.4.4 Go to "Peer to Peer I/O" page.

With peer to peer I/O activity, input channel status on one module could be actively updated to specific output channel on another module over the existing Ethernet connection. The above data exchange will be transferred automatically without any controller or programming needed.

Each channel including all the internal flags on the source module can be mapping to channel including all the internal flags on different destination modules. The Peer-to-Peer I/O activity on all the modules not only supports

the "one-to-one" mapping but also "multiple-to-one/from-one" and "one-to/from-multiple" mapping simultaneously.

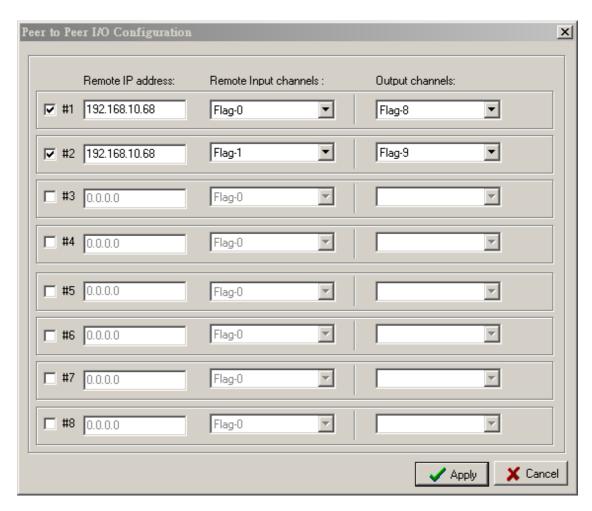


The below will guide you on how to configure peer-to-peer functions step-by-step walkthrough.

- (1) Enable Peer to Peer I/O activity
 - The Peer to Peer I/O activity is disabled by default. You could enable this function by checking "Enable peer to peer I/O" on this page.
- (2) Configure module's input channels mirrored to remote IP Select "Mirror my input channels to:", the input channel status on the input module could be assigned to send those input data including all the internal flags to one remote destination IP address as defined on the field of "Remote IP", e.g., 192.168.10.51 as the above figure.
- (3) Configure module's output channels mirrored from remote IP

 Select "Mirror my output channels from:", the output channel state on the output module could be mirrored from one remote input channels or internal flags on different destination modules. Click "Rule" and do the further assignments for those internal flags which will mirror the remote internal flags on the corresponding remotely input module as defined on the

field of "Remote IP address". It could support up to 8 rules assignment. Press "Apply" to activate the new setting.

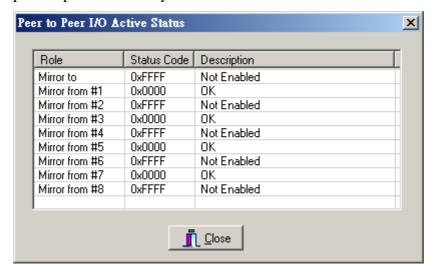


After completing all configurations for Peer-to-Peer I/O activity, click "Save" button, all the mapping configurations will be flushed into flash memory on the module. In order to extend the flash memory life, it is strongly recommended that you should save all configurations together at one time instead of saving individual setting many times. The indicator in the left of the "Save" button shows the status of being flushed into flash memory. If there was error occurred, the color become to red. Otherwise, the system will automatically restart the module and prompt you to rescan the module on the network again.



Refer to above Figure as below, in the bottom field of this "Peer to Peer I/O" page is the motoring area. It is monitoring the status both on peer-to-peer I/O activity and internal flags. Click "Active Status" button, the dialog of

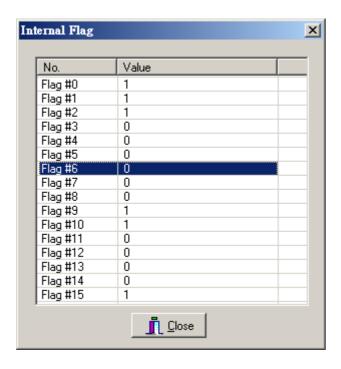
peer-to-peer I/O active status will popup to represent the status code on peer-to-peer I/O activity.



If there is error occurred, you could directly browse the latest activity from status code of peer-to-peer I/O activity. The status code is defined as below.

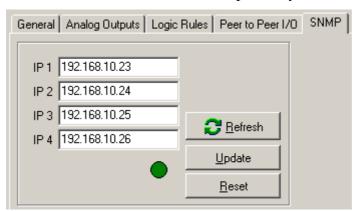
Status Code	Descriptions
0x0000	NO Errors
0x0001	Requested Content Not Satisfiable. That is, The content
	was well-formed but was unable to be followed due to
	non-satisfiable data. For example, module type associated
	with data format was NOT consistent.
0x0002	Remote Module Not Found
0xFFFF	Not Enabled the Peer-to-Peer I/O Activity

Click "Internal Flag" button, the internal flag dialog will prompt for you to browse all the internal flags. If you use internal flags as the inputs of logic rules and/or peer-to-peer I/O activity, you can dynamically change the flag values in the monitoring by double clicking the Flag# row as you selected, and then the flag values will be changed from "True" to "False", or from "False" to "True".



4.1.4.5 Go to "SNMP" page.

IP Setting: You can configure up to 4 SNMP Trap Server's IP here. Type the IP address and press "Update" to activate the new setting. Click "Refresh" to reload the current SNMP trap server's IP from registers. Once the SNMP trap has been activated by I/O login rule, you need to press "Reset" to acknowledge it and then allow the next SNMP trap activity.

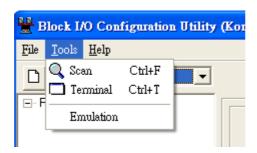


The round LED shows green when press update and the setting is correct.

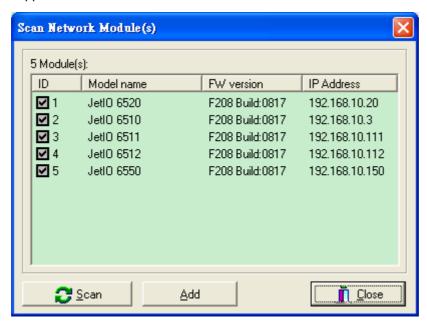
4.1.5 Emulation Mode

Block I/O Configuration Utility provides Emulation mode that allows users to know the functions it supports, and good practice for users to know how to operate block I/O configuration utility even when users don't have physical devices on hand.

4.1.4.1 Select "Tools -> Emulation".



4.1.4.2 Follow the 4.1.3 to scan the network, you'll find the models Jetl/O currently supported. Click "Add" to add the models.

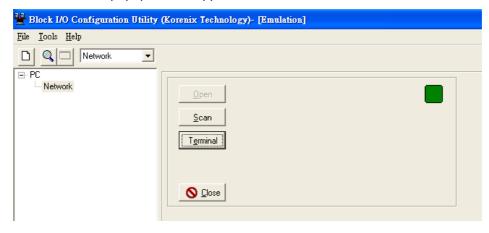


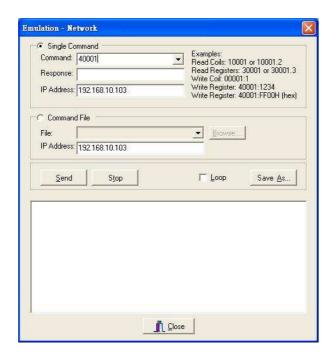
4.1.4.3 Follow the 4.1.4 to practice Jetl/O configuration. Select the model and read or write status and configurations. As to how to operate the Jetl/O configuration of other model, please refer to its manual.

4.1.6 Terminal Mode

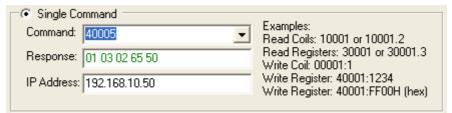
Block I/O Configuration Utility provides "Terminal" mode for user to read and write Modbus/TCP registers, thus users do not need additional tools but still can practice Modbus/TCP protocol well.

4.1.6.1 Open Terminal Mode. Click Open and then select "Terminal". The terminal emulation popup screen appears.





4.1.6.2 Single Command mode. Type the correct IP address of target unit in IP Address field, PLC address in the Command field. Then click "Enter" key. You can read the Response of the PLC address.



Note: If you type the wrong IP address, the utility will re-try the connection few times. This may take few seconds, please wait and close the popup alert screen and type the correct IP address again.

- 4.1.6.3 To read register, just type the PLC address. To write register, user needs to type the new value behind the PLC address. The example is 40001:1234 (ASCII word) or 40001:FF00h (16x Hex).
- 4.1.6.4 Command File mode. Type PLC address you want read or write in the text file.

 Type the correct IP address of target unit in IP Address field. Browse the text file to load the file.

Example: Write below commands in Modbus test.txt file and browse it.

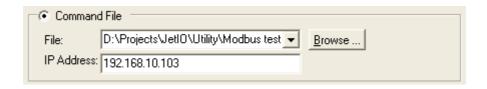
40001

40002

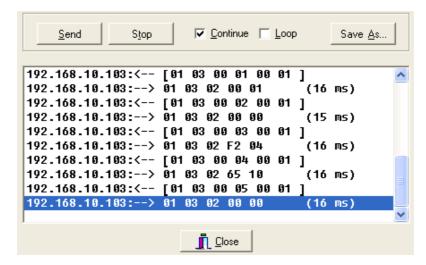
40003

40004

40005



4.1.6.5 Commands: Run Send to run the multiple commands. Run "Stop" to stop the program. Select "Continue" to run all commands once. Select "Loop" to continuously run all commands. The commands can be applied to Single Command mode and Command File mode.



The above screen shows you the result of running "Modbus test.txt" example in 4.1.6.4.

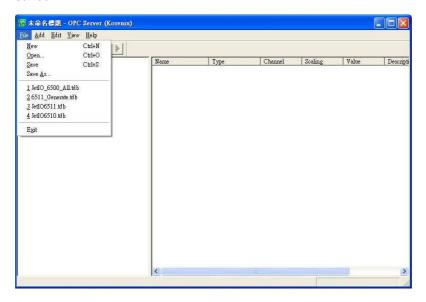
4.2 Block I/O OPC Server Utility

4.2.1 OPC Server Utility

1. Go to "Start" -> "Program", and then you can see the "Block IO Utility (Korenix) folder. There are 2 utilities are installed, Block IO OPC Server and Block IO Utility.



- 2. Open the "Block IO OPC Server".
- 3. Select "File -> New" to create new profile. Or select "File -> Open" to open profile you saved.



4. Select "Add -> New Device", the popup window "Driver Selection" will appear. (Only appear in the first time you add new device). Click "Add" and type the driver name and correct IP address. Click "OK" to next popup windows for Driver Selection. Use "Edit -> Comm Setting" can modify the parameters.

Note: Different model should have different Driver Name. We recommend user add the entire driver for all the available models you connected first.



Figure 4.1 "Add" the "New Device".



Figure 4.2 "Driver Selection Window. Click "Add..." to next popup window.

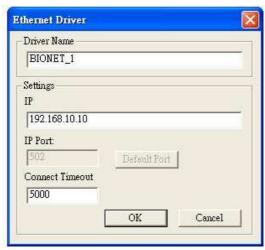


Figure 4.3 "Ethernet Driver" popup Window. Type the Driver Name and IP address for the device.

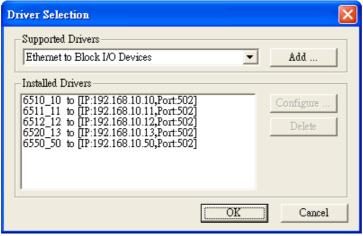
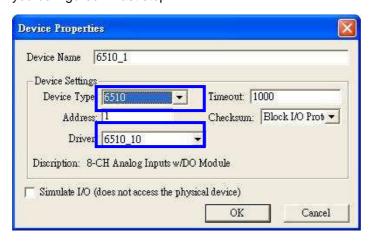


Figure 5: Example: Add all the drivers for available models. If you have 5 models over the same network, add them and give them different name for identification in next steps. Like: (Model Name/6510)_(IP address/10).

5. Type the "Device Name" and select the "Device Type" and the "Driver" in the "Device Properties" window. Device Type means the Jetl/O model name. Driver is the name you configured in last step.



6. Select "Add -> New Group" to create new group for the later new tags you'll create. Select "Add -> New Tag" and fill the "Tag Properties" in the popup window. Select the tag and "Edit -> Properties", you can modify the tag properties.



Note: The **Simulation Signal** is used when choosing Simulate I/O mode. Simulate I/O mode is selected in "Device Properties". This feature allows you to generate simulation values and run testing when you operate the OPC client. You can see the value is continuously changed. The Sine, Ramp and Random are the different type's simulation signal.

7. Select the device in the device list. Choose "Add -> Generate Tags", the utility generate all the channels' tags for the device you choose. This step can save the time to create all channels' tags.

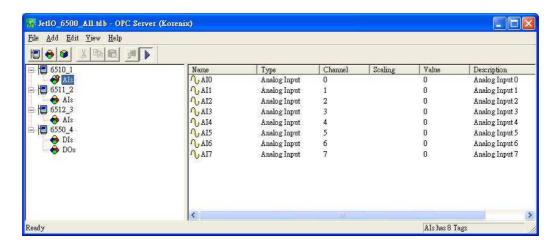
Name: The name of the channel. You can manually change this value.

Type: The input type of the channel.

Channel: The channel ID.

Value: The value of the channel, you can use "Monitor" to read them.

Description: The description of this channel, you can manually change this value.



8. Select "View -> Monitor" to monitor the status of the tags. Or you can click the "Monitor" icon in the UI.



9. Select "File -> Save" to save the profile, then your OPC Client can monitor the Jet I/O status.

4.3 SNMP

Simple Network Management Protocol (SNMP) is a protocol used for exchanging management information between network devices. SNMP is a member of the TCP/IP protocol suite. Jetl/O 6500 series acts as SNMP node, SNMP browser can discover and read/write channels' information.

An SNMP managed network consists of two main components: agents and manager. An agent is a management software module that resides in a managed switch. An agent translates the local management information from the managed device into a SNMP compatible format. The manager is the console through the network.

Jetl/O 6500 series supports Public MIB: MIB II-System. This is for SNMP browser discovering. Private MIB includes channels' information. Please refer to the appendix 1 (6.1).

SNMP Trap allows the Jetl/O to send the active alarm to trap servers. The SNMP Trap supports Device Cold Start, LAN interface Link Up trap (Common), Low and High Voltage/Current/Temperature (651x) and Logic Rules' traps (655x). You can configure this through Modbus/TCP registers or I/O Configuration utility.

4.4 Web UI

Type the IP address of the device. Then you can access the embedded web browser of the I/O server. The web browser allows you monitor the information/status of each channels.

4.5 How to Upgrade Firmware

The Jetl/O server allows you remotely upgrade the firmware to fix the known issues or to update the new software features. Device Finder provides a user-friendly environment for firmware upgrade, which includes two modes:

- Mode A (Firmware Upgrade): Used to upgrade the firmware of a Jetl/O module which is with valid firmware and workable. Device Finder supports batch upgrade in this mode. User can upgrade more than one Jetl/O device (the same model) at the same time.
- 2. Mode B (Firmware Rescue): Used to reload the firmware of a Jetl/O without firmware.

When user starts the progress of the firmware upgrade, the Jetl/O runs as the DHCP client mode to get the IP from DHCP Server and download the firmware from the server.

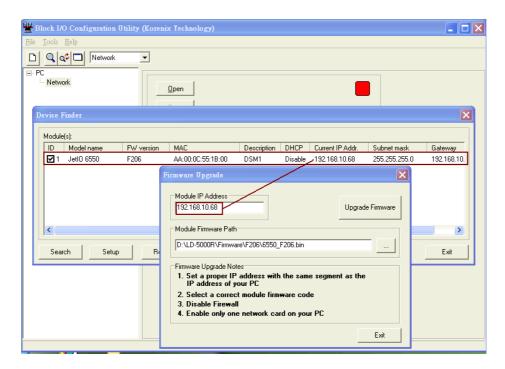
Note 1: The progress is also known as BootP, Get IP address and upgrade new firmware in the same progress. Please note that there is only one DHCP server available over the same network. Otherwise the device may get the wrong IP. Since Device Finder builds in a BOOTP server, Korenix suggests you make sure there is only one DHCP/BOOTP server on the network when you upgrade the JetIO firmware.

Upgrade Procedure for Mode A (Firmware Upgrade):

- Lunch the Block I/O utility and then select the 'Device Finder' from toolbar or select Device Finder from Tools menu to enable the Device Finder tool.
- 2. Press "Search" button to search all JetlO modules on the network and check the IP address (e.g. 192.168.10.68) of the JetlO target module.

Notes:

- (a). Disable Firewall
- (b). Enable only one network card on your PC
- (c). Set a proper IP address with the same segment as the IP address of your PC
- (d). DON'T configure more than one IP address on the network interface.
- (e). Select a correct module firmware code (i.e., 6550_Fxxx.bin for JetIO 6550)
- 3. Select the target module and click "Upgrade" to upload the new firmware, and then the 'Firmware Upgrade' dialog prompt you to do the further setting.
- 4. Select the JetlO target module from the device list of Device Finder console. You can select one more modules with the same model name to do batch upgrade.
- 5. Press "Upgrade" button to pop up the Firmware Upgrade dialog.
- 6. The default value of the "Module IP Address" field is the current IP address of the device. For batch upgrade you do not need to change this field.
- Press the browser button _____ to select a correct firmware code. Please do not
 modify the filename. Device Finder uses the filename of the firmware to identify if the
 firmware matches the model of the Jetl/O device.
- 8. Press "Upgrade Firmware" button to start upgrading the new firmware code.
- 9. The JetlO target module should be rebooted automatically after the new JetlO firmware code was upgraded successfully.



Upgrade Procedure for Mode B (Firmware Resure):

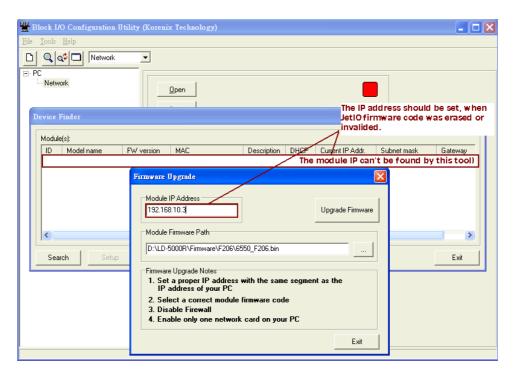
- Lunch the Block I/O utility and then select the 'Device Finder' from toolbar or select Device Finder from Tools menu to enable the Device Finder tool.
- 2. A Jetl/O with invalid firmware can not be found by search.

Notes:

- (a). Disable Firewall
- (b). Enable only one network card on your PC
- (c). Set a proper IP address with the same segment as the IP address of your PC
- (d). DON'T configure more than one IP address on the network interface.

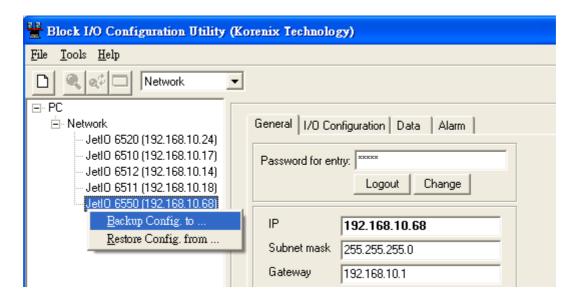
(e). Select a correct module firmware code (i.e., 6550_Fxxx.bin for JetIO 6550)

- 3. Press "Upgrade" button to pop up the Firmware Upgrade console.
- 4. Set a proper IP address for JetIO module boot loader. Please note that the IP address should be set to the same network segment of your PC.
- 5. Press the browser button ____ to select a correct JetlO firmware code. Please do not modify the filename. Device Finder uses the filename of the firmware to identify if the firmware matches the model of the Jetl/O device.
- 6. Press "Upgrade Firmware" button to start upgrading the new JetlO firmware code.
- 7. Press "Yes" to start the progress when seeing the upgrading information popup window. Press "No" to stop the progress.
- 8. The JetlO target module should be rebooted automatically after the new JetlO firmware code was upgraded successfully.

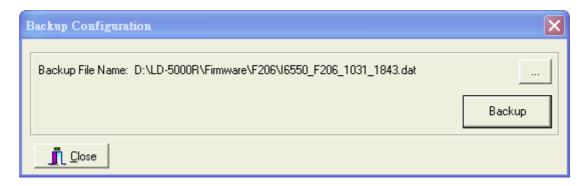


4.6 Configuration Backup/Restore, Reset Default, and Reboot

The backup/restore configuration functions are accessed by right clicking on a JetIO module in the network interface tree.



Select "Backup Config. to ..." and "Restore Config. from ..." command to backup and restore the configuration of the JetlO to/from a text file. It should be noticed that you will need to login first and then gain a privilege to do these functions.



Click "Reset to default" and "Reboot" in the popup menu to set factory default or reboot the device. It should be noticed that you need to "rescan network" after completing the "Reset to default" and "Reboot" activity.

Below is the related configuration information corresponding to JetlO's Modbus registers as well as module IP configuration for your reference (i.e., internally script command to implement backup and restore configuration activity).

JetIO-6520 Module

[Backup]

0=orange.conf

1=snmp.trap.ips

2=mis

3=#ip.conf

[mis.PLCAddr]

0=40001:40002

1=41000:41003

[orange.conf.PLCAddr]

0=40009:40010

[snmp.trap.ips.PLCAddr]

0=40029:40037

[ip.conf.Program]

key=ip.conf

var0=ip.ip

var1=ip.mask

var2=ip.gateway

var3=ip.dhcp

5 Modbus/TCP Command Set

This chapter introduces the Modbus/TCP command set Jetl/O provided. When you creating application for your SCADA/HMI or coding your own programs. The command set is helpful for you to find the value of each registers.

Following topics are covered in this chapter:

- 5.1 Introduction of Modbus/TCP Protocol
- 5.2 Jetl/O 6520 Modbus/TCP Address Mapping

5.1 Introduction of Modbus/TCP Protocol

5.1.1 Modbus/TCP Protocol

The Modbus protocol, developed by Gould-Modicon, is widely used in industrial communications to integrate PLC's, computer, terminals and other various I/O devices. Intelligent Jetl/O Server equipped with communication interface provides an Ethernet communication links with Modbus/TCP protocol support.

Modbus/TCP is a variant of the Modbus family of communication protocol. Modbus/TCP is a Master/Slave communication protocol. A master (a host PC) initiates queries, a slave (one of the Jetl/O servers) then responds by supplying the requested data to the master by using Modbus/TCP commands.

5.1.2 Function Code (FC)

The Intelligent JetI/O Server uses a subset of the standard Modbus/TCP function code to access device-dependent information. Modbus/TCP function code is defined as below.

FC	Name	Usage
01	Read Coils	Read the state of a digital output
02	Read Input Status	Read the state of a digital input
03	Read Holding Register	Read holding register in 16-bits register format
04	Read Input Registers	Read data in 16-bits register format
05	Write Coil	Write data to force a digital output ON/OFF
06	Write Single Register	Write data in 16-bits register format
15	Force Multiple Coils	Write data to force multiple consecutive coils

5.1.3 Error Checking

The utilization of the error checking will help eliminate errors caused by noise in the communication link. In Modbus/TCP mode, messages include an error-checking field that is based on a Cyclical Redundancy Check (CRC) method. The CRC filed checks the contents of the entire message. It applied regardless of any parity check method used for the individual BYTE actors of the message. The CRC value is calculated by the transmitting device, which appends the CRC to the message. The receiving device recalculates a CRC during receipt of the message, and compares the calculated value to the actual value it received in the CRC filed.

5.1.4 Exception Response

If an error occurs, the slave sends an exception response message to master consisting of the slave address, function code, exception response code and error check field. In an exception response, the slave sets the high-order bit (MSB) of the response function code to one. The exception response codes are listed below.

Code	Name	Descriptions
01	Illegal Function	The message function received is not allowable
		action.
02	Illegal Data Address	The address referenced in the data field is not valid.
03	Illegal Data Value	The value referenced at the addressed device location is no within range.
04	Slave Device Failure	An unrecoverable error occurred while the slave was attempting to perform the requested action.
05	Acknowledge	The slave has accepted the request and processing it, but a long duration of time will be required to do so.
06	Slave Device Busy	The slave is engaged in processing a long-duration program command.
07	Negative Acknowledge	The slave cannot perform the program function received in the query.
08	Memory Parity Error	The slave attempted to read extended memory, but detected a parity error in the memory.

5.2 Jetl/O 6520 Modbus/TCP Address Mapping

Note: Some of the results are using Hex (Ex: FF00H) mode, Signed (-1) mode... or others mode. This result depends on the value of the address. There are two types Modbus/TCP addresses. The protocol address is an address from 0000 to the last address of the function code you choose. The PLC address is the fixed address number of the function code. This is also defined in Modbus/TCP protocol. Please choose the type your application uses.

Should you encounter problem on reading this, please contact our technical support engineer, korecare@korenix.com

Jetl	Jetl/O 6520 Common Register Map (Holding Registers, Function Code = 03)			
Protocol Address (Hex)	PLC Address (Decimal)	Access	Description	
0000	40001	R/W	(Read/Write) Host Watch-dog enable/disable R/W:AABB AABB:0000H (disable) AABB:FF00H (enable)	
0001	40002	R/W	(Read/Write) Host Watch-dog cycle count R/W:AABB AABB:0001H~00FFH BB:1 ~255 BB x 0.1 (sec)=count cycle (sec)	
0002	40003	R/W	R: Read the host-watchdog status	

		1	
			W: Reset the host-watchdog status
			R:AABB
			AABB:0000H (remote module OK)
			AABB:FF00H (host-watchdog fail)
			W: AABB
			AABB:FF00H(reset)
0003	40004	R	Read the firmware version
			R:AAAA
			AAAA:F204 (HEX)
0004	40005	R	Read module name
			R:AAAA
			AAAA: 6520 (HEX)
0005	40006	R	Read reset status
			R:AABB
			AABB:0000H (after using this read command)
			AABB:0001H(The value is equal to0001H after reset
			module)
0006	40007	R	R: always 0
0007	40008	R	R: always 0
8000	40009	R/W	Read/Write DA Slew Rate Code
			R: Read slew rate
			W: Write slew rate
			R:ABCD
			W:ABCD
			A:0~B for channel 3 (ref. the Appedix 6.2)
			B:0~B for channel 2
			C:0~B for channel 1
			D:0~B for channel 0
			0:immediate change
0009	40010	R/W	Output range (address low byte)
			Code:0~2
			R/W:ABCD
			A:0~2 for Channel 3
			B:0~2 for Channel 2
			C:0~2 for Channel 1
			D:0~2 for Channel 0
_			0: 0~+10V 1: -10V~+10V 2: 0~20mA
000A	40011	R/W	Read/Write power on DA value of channel 0
			R:0AAA
			AAA: Hexadecimal DA value
			Write present DA value as power on value of channel 0
			W: AABB
	100:5		AABB:FF00H
000B	40012	R/W	Read/Write power on DA value of channel 1
			R:0AAA
			AAA: Hexadecimal DA value
			Write present DA value as power on value of channel 1
			W: AABB
			AABB:FF00H

000C	40013	R/W	Pead/Mrite power on DA value of channel 2
JUUC	40013	FX/VV	Read/Write power on DA value of channel 2 R:0AAA
			AAA: Hexadecimal DA value
			Write present DA value as power on value of channel 2
			W: AABB
000D	10011	DAM	AABB:FF00H
000D	40014	R/W	Read/Write power on DA value of channel 3
			R:0AAA
			AAA: Hexadecimal DA value
			Write present DA value as power on value of channel 3
			W: AABB
			AABB:FF00H
000E	40015	R/W	Read/Write DA offset (Zero) Calibration Coefficients of
			channel #0
			R:AABB
			AABB: offset (Zero) Calibration Coefficient value
			W: FF00H (offset Calibration)
000F	40016	R/W	Read/Write DA span Calibration Coefficients of channel
			#0
			R:AABB
			AABB: span Calibration Coefficient value
			W: FF00H (span Calibration)
0010	40017	R/W	Read/Write DA offset (Zero) Calibration Coefficients of
			channel #1
			R:AABB
			AABB: offset (Zero) Calibration Coefficient value
			W: FF00H (offset Calibration)
0011	40018	R/W	Read/Write DA span Calibration Coefficients of channel
			#1
			R:AABB
			AABB: span Calibration Coefficient value
			W: FF00H (span Calibration)
0012	40019	R/W	Read/Write DA offset (Zero) Calibration Coefficients of
			channel #2
			R:AABB
			AABB: offset (Zero) Calibration Coefficient value
			W: FF00H (offset Calibration)
0013	40020	R/W	Read/Write DA span Calibration Coefficients of channel
			#2
			R:AABB
			AABB: span Calibration Coefficient value
			W: FF00H (span Calibration)
0014	40021	R/W	Read/Write DA offset (Zero) Calibration Coefficients of
	150= 1		channel #3
			R:AABB
			AABB: offset (Zero) Calibration Coefficient value
			W: FF00H (offset Calibration)
0015	40022	R/W	Read/Write DA span Calibration Coefficients of channel
0010	40022	FX/ V V	·
			#3

			D AADD
			R:AABB
			AABB: span Calibration Coefficient value
			W: FF00H (span Calibration)
0016	40023	R/W	Analog output voltage of Channel #0
			Note:
			R: read last DA output voltage value
			W: AAAA
			AAAA: 0~10000 means (0~10V) for output range=0
			AAAA: -10000~10000 means (-10~10V) for output
			range=1
			AAAA: 0~20000 means (0~20mA) for output range=2
0017	40024	R/W	Analog output voltage of Channel #1
			Note:
			R: read last DA output voltage value
			W: AAAA
			AAAA: 0~10000 means (0~10V) for output range=0
			AAAA: -10000~10000 means (-10~10V) for output
			range=1
			AAAA: 0~20000 means (0~20mA) for output range=2
0018	40025	R/W	· · · · · · · · · · · · · · · · · · ·
0016	40025	IK/VV	Analog output voltage of Channel #2
			Note:
			R: read last DA output voltage value
			W: AAAA
			AAAA: 0~10000 means (0~10V) for output range=0
			AAAA: -10000~10000 means (-10~10V) for output
			range=1
			AAAA: 0~20000 means (0~20mA) for output range=2
0019	40026	R/W	Analog output voltage of Channel #3
			Note:
			R: read last DA output voltage value
			W: AAAA
			AAAA: 0~10000 means (0~10V) for output range=0
			AAAA: -10000~10000 means (-10~10V) for output
			range=1
			AAAA: 0~20000 means (0~20mA) for output range=2
001A	40027	W	Positive Trim operation of channel N
			W:0ABB
			A: Channel number (0~3)
			BB: Trim count(00~FF)
001B	40028	W	Negative Trim operation of channel N
			W:0ABB
			A: Channel number (0~3)
			BB: Trim count(00~FF)
001C	40029	R/W	Read/write SNMP Trap Number
			R/W:AAAA
			AAAA:0~4
22.17	10055	5	0: close SNMP trap
001D	40030	R/W	(Read/Write)SNMP Trap IP1 Lo-Word
			R: AABB(hex)
			W: AABB(hex)

			IP=X.X.AA.BB
001E	40031	R/W	(Read/Write)SNMP Trap IP1 Hi-Word R: AAB R/W B(hex) W: AABB(hex)
001F	40032	R/W	IP=AA.BB.X.X (Read/Write)SNMP Trap IP2 Lo-Word R: AABB(hex) W: AABB(hex)
0020	40033	R/W	IP=X.X.AA.BB (Read/Write)SNMP Trap IP2 Hi-Word R: AABB(hex) W: AABB(hex) IP=AA.BB.X.X
0021	40034	R/W	(Read/Write)SNMP Trap IP3 Lo-Word R: AABB(hex) W: AABB(hex) IP=X.X.AA.BB
0022	40035	R/W	(Read/Write)SNMP Trap IP3 Hi-Word R: AABB(hex) W: AABB(hex) IP=AA.BB.X.X
0023	40036	R/W	(Read/Write)SNMP Trap IP4 Lo-Word R: AABB(hex) W: AABB(hex) IP=X.X.AA.BB
0024	40037	R/W	(Read/Write)SNMP Trap IP4 Hi-Word R: AABB(hex) W: AABB(hex) IP=AA.BB.X.X
0025	40038	R	Read offset Calibration Coefficients of channel #0 R:AABB (factory calibration value) Output range: 0~10v
0026	40039	R	Read span Calibration Coefficients of channel #0 R:AABB (factory calibration value) Output range: 0~10v
0027	40040	R	Read offset Calibration Coefficients of channel #0 R:AABB (factory calibration value) Output range: -10v~+10v
0028	40041	R	Read span Calibration Coefficients of channel #0 R:AABB (factory calibration value) Output range: -10v~+10v
0029	40042	R	Read offset Calibration Coefficients of channel #0 R:AABB (factory calibration value) Output range: 0~20mA
002A	40043	R	Read span Calibration Coefficients of channel #0 R:AABB (factory calibration value) Output range: 0~20mA
002B	40044	R	Read offset Calibration Coefficients of channel #1 R:AABB (factory calibration value) Output range: 0~10v

002C	40045	R	Read span Calibration Coefficients of channel #1
			R:AABB (factory calibration value)
			Output range: 0~10v
002D	40046	R	Read offset Calibration Coefficients of channel #1
			R:AABB (factory calibration value)
			Output range: -10v~+10v
002E	40047	R	Read span Calibration Coefficients of channel #1
			R:AABB (factory calibration value)
002F	40048	R	Output range: -10v~+10v
0021	40040		Read offset Calibration Coefficients of channel #1
			R:AABB (factory calibration value)
0030	40049	R	Output range: 0~20mA Read span Calibration Coefficients of channel #1
			R:AABB (factory calibration value)
			Output range: 0~20mA
0031	40050	R	Read offset Calibration Coefficients of channel #2
			R:AABB (factory calibration value)
			Output range: 0~10v
0032	40051	R	Read span Calibration Coefficients of channel #2
			R:AABB (factory calibration value)
0022	400.72		Output range: 0~10v
0033	40052	R	Read offset Calibration Coefficients of channel #2
			R:AABB (factory calibration value)
0034	40053	R	Output range: -10v~+10v
0034	40033		Read span Calibration Coefficients of channel #2
			R:AABB (factory calibration value) Output range: -10v~+10v
0035	40054	R	Read offset Calibration Coefficients of channel #2
			R:AABB (factory calibration value)
			Output range: 0~20mA
0036	40055	R	Read span Calibration Coefficients of channel #2
			R:AABB (factory calibration value)
			Output range: 0~20mA
0037	40056	R	Read offset Calibration Coefficients of channel #3
			R:AABB (factory calibration value)
0020	40057		Output range: 0~10v
0038	40057	R	Read span Calibration Coefficients of channel #3
			R:AABB (factory calibration value)
0039	40058	R	Output range: 0~10v
0037	40000		Read offset Calibration Coefficients of channel #3
			R:AABB (factory calibration value)
			Output range: -10v~+10v

003A	40059	R	Read span Calibration Coefficients of channel #3
			R:AABB (factory calibration value)
			Output range: -10v~+10v
003B	40060	R	Read offset Calibration Coefficients of channel #3
			R:AABB (factory calibration value)
003C	40061	R	Output range: 0~20mA
003C	40061	K	Read span Calibration Coefficients of channel #3
			R:AABB (factory calibration value) Output range: 0~20mA
003D	40062	W	(Write) Repeat enable SNMP Trap
			W: FF00(hex)
01F4	40501	R/W	(Read/Write) RuleEnable Logic 0~15
			R: xxxxxxx xxxxxxx(bit)
			W: xxxxxxx xxxxxxx(bit)
			0: disable
			1: enable (write to Flash RAM)
01F5	40502	R/W	(Read/Write) Select "OR" or "AND"
			R:AAAA(hex)
			W: AAAA (hex)
			AAAA: xxxxxxxx xxxxxxxx
			X: 0 "OR" Logic 1 "AND" Logic (write to
			Flash RAM)
01F6	40503	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 0
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
01F7	40504	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 1
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to

			Flash RAM)
01F8	40505	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 2
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
01F9	40506	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 3
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
01FA	40507	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 4
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
01FB	40508	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 5
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)

01FC	40509	R/W	(Read/Write) Select condition Auxflag channel for Rule 6 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
01FD	40510	R/W	Flash RAM) (Read/Write) Select condition Auxflag channel for Rule 7
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to Flash RAM)
01FE	40511	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 8
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to Flash RAM)
01FF	40512	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 9
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0200	40513	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 10

			D. ADCD(1)
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0201	40514	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 11
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0202	40515	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 12
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0203	40516	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 13
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0204	40517	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 14
			R: ABCD(hex)
			W: ABCD(hex)

			A 11.1 HOT A 61 61
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0205	40518	R/W	(Read/Write) Select condition Auxflag channel for
			Rule 15
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0206	40519	R/W	(Read/Write) Select Operators condition for Rule 0
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			, , ,
			3:Hi-Lo (Auxflag)
0007	40.720	D /777	(write to Flash RAM)
0207	40520	R/W	(Read/Write) Select Operators condition for Rule 1
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0 : ON (Auxflag)

			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
0208	40521	R/W	(Read/Write) Select Operators condition for Rule 2
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0: ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
0209	40522	R/W	(Read/Write) Select Operators condition for Rule 3
0207	40322	IX/ VV	R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0: ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
020A	40523	R/W	(Read/Write) Select Operators condition for Rule 4
020A	70323	10/ 44	R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			value.

			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
0205	10.50	D /II.	(write to Flash RAM)
020B	40524	R/W	(Read/Write) Select Operators condition for Rule 5
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
020C	40525	R/W	(Read/Write) Select Operators condition for Rule 6
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
020D	40526	R/W	(Read/Write) Select Operators condition for Rule 7
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
		j	2. Sperators condition no Logic

		İ	Value:
•			
			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
020E	40527	R/W	(Read/Write) Select Operators condition for Rule 8
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			C: Operators condition #1 Logic
			D: Operators condition #0 Logic
			Value:
			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
020F	40528	R/W	(Read/Write) Select Operators condition for Rule 9
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators condition #3 Logic
			B: Operators condition #2 Logic
			Value:
			0 : ON (Auxflag)
			1:OFF (Auxflag)
			2:Lo-Hi (Auxflag)
			3:Hi-Lo (Auxflag)
			(write to Flash RAM)
0210	40529	R/W	(Read/Write) Select Operators condition for Rule
			10
	·	l	
			R: ABCD(hex)
020F	40528	R/W	(Read/Write) Select Operators condition for Rule 9 R: ABCD(hex) W: ABCD(hex) A: Operators condition #3 Logic B: Operators condition #2 Logic C: Operators condition #1 Logic D: Operators condition #0 Logic Value: 0: ON (Auxflag)

		I		
			A: Operators condition	•
			B: Operators condition	_
			C: Operators condition	#1 Logic
			D: Operators condition	#0 Logic
			Value:	
			0: ON (Auxflag)	
			1:OFF (Auxflag)	
			2:Lo-Hi (Auxflag)	
			3:Hi-Lo (Auxflag)	
			(write to Flash RAM)	
0211	40530	R/W	(Read/Write) Select Ope	erators condition for Rule
			11	
			R: ABCD(hex)	
			W: ABCD(hex)	
			A: Operators condition	#3 Logic
			B: Operators condition	#2 Logic
			C: Operators condition	#1 Logic
			D: Operators condition	#0 Logic
			Value:	
			0: ON (Auxflag)	
			1:OFF (Auxflag)	
			2:Lo-Hi (Auxflag)	
			3:Hi-Lo (Auxflag)	
			(write to Flash RAM)	
0212	40531	R/W	(Read/Write) Select Ope	erators condition for Rule
			12	
			R: ABCD(hex)	
			W: ABCD(hex)	
			A: Operators condition	#3 Logic
			B: Operators condition	#2 Logic
			C: Operators condition	#1 Logic
			D: Operators condition	#0 Logic
			Value:	
			0 : ON (Auxflag)	
			1:OFF (Auxflag)	
			2:Lo-Hi (Auxflag)	
			3:Hi-Lo (Auxflag)	
			(write to Flash RAM)	

0213	40532	R/W	(Read/Write) Select Operators condition for Rule 13 R: ABCD(hex) W: ABCD(hex) A: Operators condition #3 Logic B: Operators condition #2 Logic C: Operators condition #1 Logic D: Operators condition #0 Logic Value: 0: ON (Auxflag) 1:OFF (Auxflag) 2:Lo-Hi (Auxflag) 3:Hi-Lo (Auxflag)
0214	40533	R/W	(write to Flash RAM) (Read/Write) Select Operators condition for Rule 14 R: ABCD(hex) W: ABCD(hex) A: Operators condition #3 Logic B: Operators condition #2 Logic C: Operators condition #1 Logic D: Operators condition #0 Logic Value: 0: ON (Auxflag) 1:OFF (Auxflag) 2:Lo-Hi (Auxflag) 3:Hi-Lo (Auxflag) (write to Flash RAM)
0215	40534	R/W	(Read/Write) Select Operators condition for Rule 15 R: ABCD(hex) W: ABCD(hex) A: Operators condition #3 Logic B: Operators condition #2 Logic C: Operators condition #1 Logic D: Operators condition #0 Logic Value: 0: ON (Auxflag)

1:OFF (Auxflag) 2:Lo-Hi (Auxflag) 3:Hi-Lo (Auxflag) (write to Flash RAM) 0216	
3:Hi-Lo (Auxflag) (write to Flash RAM) 0216	
(write to Flash RAM) 0216 40535 R/W (Read/Write) Select THEN Auxflag(or AO c action for Rule 0 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	
0216 R/W (Read/Write) Select THEN Auxflag(or AO condition for Rule 0 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	
action for Rule 0 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	
R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	ite to
W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(w: Flash RAM)	ite to
A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	ite to
B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	rite to
C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(with Flash RAM)	ite to
D: condition #0 Logic Auxflag Channel(with Flash RAM)	rite to
Flash RAM)	rite to
0217 40536 R/W (Read/Write) Select THEN Auxflag(or AO c	
	nannel)
action for Rule 1	
R: ABCD(hex)	
W: ABCD(hex)	
A: condition #3 Logic Auxflag Channel	
B: condition #2 Logic Auxflag Channel	
C: condition #1 Logic Auxflag Channel	
D: condition #0 Logic Auxflag Channel(wi	ite to
Flash RAM)	
0218 40537 R/W (Read/Write) Select THEN Auxflag(or AO c	nannel)
action for Rule 2	
R: ABCD(hex)	
W: ABCD(hex)	
A: condition #3 Logic Auxflag Channel	
B: condition #2 Logic Auxflag Channel	
C: condition #1 Logic Auxflag Channel	
D: condition #0 Logic Auxflag Channel(w	ite to
Flash RAM)	
0219 40538 R/W (Read/Write) Select THEN Auxflag(or AO c	nannel)
action for Rule 3	
R: ABCD(hex)	
W: ABCD(hex)	
A: condition #3 Logic Auxflag Channel	
B: condition #2 Logic Auxflag Channel	
C: condition #1 Logic Auxflag Channel	

			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
021A	40539	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 4
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
021B	40540	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 5
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
021C	40541	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 6
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
021D	40542	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 7
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)

021E	40543	R/W	(Read/Write) Select THEN Auxflag(or AO channel) action for Rule 8 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(write to
021F	40544	R/W	Flash RAM) (Read/Write) Select THEN Auxflag(or AO channel) action for Rule 9 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(write to Flash RAM)
0220	40545	R/W	(Read/Write) Select THEN Auxflag(or AO channel) action for Rule 10 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(write to Flash RAM)
0221	40546	R/W	(Read/Write) Select THEN Auxflag(or AO channel) action for Rule 11 R: ABCD(hex) W: ABCD(hex) A: condition #3 Logic Auxflag Channel B: condition #2 Logic Auxflag Channel C: condition #1 Logic Auxflag Channel D: condition #0 Logic Auxflag Channel(write to Flash RAM)
0222	40547	R/W	(Read/Write) Select THEN Auxflag(or AO channel) action for Rule 12

			D. ADCD/hov)
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0223	40548	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 13
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0224	40549	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 14
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0225	40550	R/W	(Read/Write) Select THEN Auxflag(or AO channel)
			action for Rule 15
			R: ABCD(hex)
			W: ABCD(hex)
			A: condition #3 Logic Auxflag Channel
			B: condition #2 Logic Auxflag Channel
			C: condition #1 Logic Auxflag Channel
			D: condition #0 Logic Auxflag Channel(write to
			Flash RAM)
0226	40551	R/W	(Read/Write) Select Operators Action for Rule 0
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic

			D. On anatons Action #2 I:-
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
0227	40552	R/W	(Read/Write) Select Operators Action for Rule 1
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
0228	40553	R/W	(Read/Write) Select Operators Action for Rule 2
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
			J. DIAMI Trap (WITC to Flash KAMI)

0220	40554	D/W	(Day 1/1/1/42) Calast One of A 41 C D 1 2
0229	40554	R/W	(Read/Write) Select Operators Action for Rule 3
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
022A	40555	R/W	(Read/Write) Select Operators Action for Rule 4
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
022B	40556	R/W	(Read/Write) Select Operators Action for Rule 5
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0: ON (Auxflag)
			_
			1: OFF (Auxflag)

			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
022C	40557	R/W	(Read/Write) Select Operators Action for Rule 6
0220	40337	IX/ VV	R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0: ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
022D	40558	R/W	(Read/Write) Select Operators Action for Rule 7
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
022E	40559	R/W	(Read/Write) Select Operators Action for Rule 8
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
1			
			B: Operators Action #2 Logic

022F	40560	R/W	D: Operators Action #0 Logic Value: 0: ON (Auxflag) 1: OFF (Auxflag) 2: Analog Output 3: reserved 4: reserved 5: SNMP Trap (write to Flash RAM) (Read/Write) Select Operators Action for Rule 9 R: ABCD(hex) W: ABCD(hex) A: Operators Action #3 Logic
			B: Operators Action #2 Logic C: Operators Action #1 Logic D: Operators Action #0 Logic Value: 0: ON (Auxflag) 1: OFF (Auxflag) 2: Analog Output 3: reserved 4: reserved 5: SNMP Trap (write to Flash RAM)
0230	40561	R/W	(Read/Write) Select Operators Action for Rule 10 R: ABCD(hex) W: ABCD(hex) A: Operators Action #3 Logic B: Operators Action #2 Logic C: Operators Action #1 Logic D: Operators Action #0 Logic Value: 0: ON (Auxflag) 1: OFF (Auxflag) 2: Analog Output 3: reserved 4: reserved 5: SNMP Trap (write to Flash RAM)
0231	40562	R/W	(Read/Write) Select Operators Action for Rule 11 R: ABCD(hex)

	1		W. A DCD (L.)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0: ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
0232	40563	R/W	(Read/Write) Select Operators Action for Rule 12
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0: ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
0233	40564	R/W	(Read/Write) Select Operators Action for Rule 13
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved

			4: reserved
			5: SNMP Trap (write to Flash RAM)
0234	40565	R/W	(Read/Write) Select Operators Action for Rule 14
			R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0 : ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
0235	40566	R/W	(Read/Write) Select Operators Action for Rule 15
0233	40300	10/ **	R: ABCD(hex)
			W: ABCD(hex)
			A: Operators Action #3 Logic
			B: Operators Action #2 Logic
			C: Operators Action #1 Logic
			D: Operators Action #0 Logic
			Value:
			0: ON (Auxflag)
			1: OFF (Auxflag)
			2: Analog Output
			3: reserved
			4: reserved
			5: SNMP Trap (write to Flash RAM)
0236	40567	R/W	(Read/Write) Condition Enable for Rule 0~3
0230	10307		R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$:disable 1:enable rule 3
			B: xxxx x= 0:disable 1:enable rule 2
			C: xxxx x= 0:disable 1:enable rule 1
			D: xxxx x=0:disable 1:enable rule 0
			(write to Flash RAM)
			(1110 10 1 10011 10 1111)

0237	40568	R/W	(Read/Write) Condition Enable for Rule 4~7
0237	10300	10, 11	R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$:disable 1:enable rule 7
			B: xxxx x= 0:disable 1:enable rule 6
			C: xxxx x= 0:disable 1:enable rule 5
			D: xxxx x= 0:disable 1:enable rule 4
0220	40560	D/W	(write to Flash RAM)
0238	40569	R/W	(Read/Write) Condition Enable for Rule 8~11
			R: ABCD(hex)
			W: ABCD(hex)
			A: xxxx x= 0:disable 1:enable rule 3
			B: xxxx x= 0:disable 1:enable rule 2
			C: xxxx x= 0:disable 1:enable rule 1
			D: $xxxx = 0$: disable 1:enable rule 0
			(write to Flash RAM)
0239	40570	R/W	(Read/Write) Condition Enable for Rule 12~15
			R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$: disable 1: enable rule 7
			B: $xxxx = 0$: disable 1: enable rule 6
			C: xxxx x= 0:disable 1:enable rule 5
			D: xxxx x= 0:disable 1:enable rule 4
			(write to Flash RAM)
023A	40571	R/W	(Read/Write) Action Enable for Rule 0~3
			R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$: disable 1: enable rule 3
			B: xxxx x= 0:disable 1:enable rule 2
			C: xxxx x= 0:disable 1:enable rule 1
			D: xxxx x= 0:disable 1:enable rule 0
			(write to Flash RAM)
023B	40572	R/W	(Read/Write) Action Enable for Rule 4~7
			R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$: disable 1: enable rule 7
			B: xxxx x= 0:disable 1:enable rule 6
			C: xxxx x= 0:disable 1:enable rule 5

			D: xxxx x= 0:disable 1:enable rule 4
			(write to Flash RAM)
023C	40573	R/W	(Read/Write) Action Enable for Rule 8~11
			R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$: disable 1: enable rule 3
			B: $xxxx = 0$: disable 1: enable rule 2
			C: xxxx x= 0:disable 1:enable rule 1
			D: xxxx x=0:disable 1:enable rule 0
			(write to Flash RAM)
023D	40574	R/W	(Read/Write) Action Enable for Rule 12~15
			R: ABCD(hex)
			W: ABCD(hex)
			A: $xxxx = 0$:disable 1:enable rule 7
			B: $xxxx = 0$:disable 1:enable rule 6
			C: xxxx x= 0:disable 1:enable rule 5
			D: xxxx x= 0:disable 1:enable rule 4
			(write to Flash RAM)
023E	40575	R/W	(Read/Write) set "then logic" AO value for rule 0
			#0
			R/W: ABCD (write to Flash RAM)
023F	40576	R/W	(Read/Write) set "then logic" AO value for rule 0
			#1
			R/W: ABCD (write to Flash RAM)
0240	40577	R/W	(Read/Write) set "then logic" AO value for rule 0
			#2
			R/W: ABCD (write to Flash RAM)
0241	40578	R/W	(Read/Write) set "then logic" AO value for rule 0
			#3
			R/W: ABCD (write to Flash RAM)
0242	40579	R/W	(Read/Write) set "then logic" AO value for rule 1
			#0
			R/W: ABCD (write to Flash RAM)
0243	40580	R/W	(Read/Write) set "then logic" AO value for rule 1
			#1
			R/W: ABCD (write to Flash RAM)
0244	40581	R/W	(Read/Write) set "then logic" AO value for rule 1
			#2

			R/W: ABCD (write to Flash RAM)
0245	40582	R/W	(Read/Write) set "then logic" AO value for rule 1
			#3
			R/W: ABCD (write to Flash RAM)
0246	40583	R/W	(Read/Write) set "then logic" AO value for rule 2
			#0
			R/W: ABCD (write to Flash RAM)
0247	40584	R/W	(Read/Write) set "then logic" AO value for rule 2
			#1
			R/W: ABCD (write to Flash RAM)
0248	40585	R/W	(Read/Write) set "then logic" AO value for rule 2
			#2
			R/W: ABCD (write to Flash RAM)
0249	40586	R/W	(Read/Write) set "then logic" AO value for rule 2
			#3
			R/W: ABCD (write to Flash RAM)
024A	40587	R/W	(Read/Write) set "then logic" AO value for rule 3
			#0
			R/W: ABCD (write to Flash RAM)
024B	40588	R/W	(Read/Write) set "then logic" AO value for rule 3
			#1
			R/W: ABCD (write to Flash RAM)
024C	40589	R/W	(Read/Write) set "then logic" AO value for rule 3
			#2
00.45	40.700	D /III	R/W: ABCD (write to Flash RAM)
024D	40590	R/W	(Read/Write) set "then logic" AO value for rule 3
			#3
0245	40501	D/W/	R/W: ABCD (write to Flash RAM)
024E	40591	R/W	(Read/Write) set "then logic" AO value for rule 4
			#0 PANA ARCD (write to Fleeh RAM)
024F	40502	R/W	R/W: ABCD (write to Flash RAM)
U24Γ	40592	N/ W	(Read/Write) set "then logic" AO value for rule 4
			R/W: ABCD (write to Flash RAM)
0250	40593	R/W	(Read/Write) set "then logic" AO value for rule 4
0230	70373	IN/ W	#2
			R/W: ABCD (write to Flash RAM)
			IN W. ADCD (WHIC IO FIASH KAIVI)

0251	40594	R/W	(Read/Write) set "then logic" AO value for rule 4
			R/W: ABCD (write to Flash RAM)
0252	40595	R/W	(Read/Write) set "then logic" AO value for rule 5
			#0
			R/W: ABCD (write to Flash RAM)
0253	40596	R/W	(Read/Write) set "then logic" AO value for rule 5
			#1
			R/W: ABCD (write to Flash RAM)
0254	40597	R/W	(Read/Write) set "then logic" AO value for rule 5
			#2
			R/W: ABCD (write to Flash RAM)
0255	40598	R/W	(Read/Write) set "then logic" AO value for rule 5
			#3
			R/W: ABCD (write to Flash RAM)
0256	40599	R/W	(Read/Write) set "then logic" AO value for rule 6
			#0
00.55	10.500	D /III	R/W: ABCD (write to Flash RAM)
0257	40600	R/W	(Read/Write) set "then logic" AO value for rule 6
			#1
0258	40601	R/W	R/W: ABCD (write to Flash RAM)
0238	40001	K/W	(Read/Write) set "then logic" AO value for rule 6
			R/W: ABCD (write to Flash RAM)
0259	40602	R/W	(Read/Write) set "then logic" AO value for rule 6
0237	40002	TC/ VV	#3
			R/W: ABCD (write to Flash RAM)
025A	40603	R/W	(Read/Write) set "then logic" AO value for rule 7
			#0
			R/W: ABCD (write to Flash RAM)
025B	40604	R/W	(Read/Write) set "then logic" AO value for rule 7
			#1
			R/W: ABCD (write to Flash RAM)
025C	40605	R/W	(Read/Write) set "then logic" AO value for rule 7
			#2
			R/W: ABCD (write to Flash RAM)
025D	40606	R/W	(Read/Write) set "then logic" AO value for rule 7
			#3

			R/W: ABCD (write to Flash RAM)
025E	40607	R/W	(Read/Write) set "then logic" AO value for rule 8
			#0
			R/W: ABCD (write to Flash RAM)
025F	40608	R/W	(Read/Write) set "then logic" AO value for rule 8
			#1
			R/W: ABCD (write to Flash RAM)
0260	40609	R/W	(Read/Write) set "then logic" AO value for rule 8
			#2
			R/W: ABCD (write to Flash RAM)
0261	40610	R/W	(Read/Write) set "then logic" AO value for rule 8
			#3
			R/W: ABCD (write to Flash RAM)
0262	40611	R/W	(Read/Write) set "then logic" AO value for rule 9
			#0
			R/W: ABCD (write to Flash RAM)
0263	40612	R/W	(Read/Write) set "then logic" AO value for rule 9
			#1
			R/W: ABCD (write to Flash RAM)
0264	40613	R/W	(Read/Write) set "then logic" AO value for rule 9
			#2
			R/W: ABCD (write to Flash RAM)
0265	40614	R/W	(Read/Write) set "then logic" AO value for rule 9
			#3
00.55	10.51.5	D /III	R/W: ABCD (write to Flash RAM)
0266	40615	R/W	(Read/Write) set "then logic" AO value for rule 10
			#0
0067	40.61.6	D/W	R/W: ABCD (write to Flash RAM)
0267	40616	R/W	(Read/Write) set "then logic" AO value for rule
			10#1 PAYA A BCD (write to Fleeb BAM)
0268	40617	R/W	R/W: ABCD (write to Flash RAM) (Pand/Write) set "then logic" A O value for rule
0200	4001/	K/W	(Read/Write) set "then logic" AO value for rule 10#2
			R/W: ABCD (write to Flash RAM)
0269	40618	R/W	(Read/Write) set "then logic" AO value for rule
0207	70010	IX/ VV	10#3
			R/W: ABCD (write to Flash RAM)
			M W. ADCD (WHIC to Flash KAIVI)

026A	40619	R/W	(Read/Write) set "then logic" AO value for rule 11
			#0
			R/W: ABCD (write to Flash RAM)
026B	40620	R/W	(Read/Write) set "then logic" AO value for rule 11
			#1
			R/W: ABCD (write to Flash RAM)
026C	40621	R/W	(Read/Write) set "then logic" AO value for rule 11
			#2
			R/W: ABCD (write to Flash RAM)
026D	40622	R/W	(Read/Write) set "then logic" AO value for rule 11
			#3
			R/W: ABCD (write to Flash RAM)
026E	40623	R/W	(Read/Write) set "then logic" AO value for rule 12
			#0
			R/W: ABCD (write to Flash RAM)
026F	40624	R/W	(Read/Write) set "then logic" AO value for rule 12
			#1
			R/W: ABCD (write to Flash RAM)
0270	40625	R/W	(Read/Write) set "then logic" AO value for rule 12
			#2
			R/W: ABCD (write to Flash RAM)
0271	40626	R/W	(Read/Write) set "then logic" AO value for rule 12
			#3
			R/W: ABCD (write to Flash RAM)
0272	40627	R/W	(Read/Write) set "then logic" AO value for rule 13
			#0
			R/W: ABCD (write to Flash RAM)
0273	40628	R/W	(Read/Write) set "then logic" AO value for rule 13
			#1
			R/W: ABCD (write to Flash RAM)
0274	40629	R/W	(Read/Write) set "then logic" AO value for rule 13
			#2
			R/W: ABCD (write to Flash RAM)
0275	40630	R/W	(Read/Write) set "then logic" AO value for rule 13
			#3
			R/W: ABCD (write to Flash RAM)
0276	40631	R/W	(Read/Write) set "then logic" AO value for rule 14
			#0

			R/W: ABCD (write to Flash RAM)
0277	40632	R/W	(Read/Write) set "then logic" AO value for rule 14
			#1
			R/W: ABCD (write to Flash RAM)
0278	40633	R/W	(Read/Write) set "then logic" AO value for rule 14
			#2
			R/W: ABCD (write to Flash RAM)
0279	40634	R/W	(Read/Write) set "then logic" AO value for rule 14
			#3
			R/W: ABCD (write to Flash RAM)
027A	40635	R/W	(Read/Write) set "then logic" AO value for rule 15
			#0
			R/W: ABCD (write to Flash RAM)
027B	40636	R/W	(Read/Write) set "then logic" AO value for rule 15
			#1
			R/W: ABCD (write to Flash RAM)
027C	40637	R/W	(Read/Write) set "then logic" AO value for rule 15
			#2
			R/W: ABCD (write to Flash RAM)
027D	40638	R/W	(Read/Write) set "then logic" AO value for rule 15
			#3
			R/W: ABCD (write to Flash RAM)
027E	40639	R/W	(Read/Write) select condition channel for PTP
			R/W: ABCD (write to Flash RAM)
			A: condition #3
			B: condition #2
			C: condition #1
0055	10.510	D /III	D: condition #0
027F	40640	R/W	(Read/Write) select condition channel for PTP
			R/W: ABCD (write to Flash RAM)
			A: condition #7
			B: condition #6
			C: condition #5
0290	40641	D /337	D: condition #4 (Pand/Write) select action shannel for PTP
0280	40641	R/W	(Read/Write) select action channel for PTP
			R/W: ABCD (write to Flash RAM) A: condition #3
			B: condition #2

			C: condition #1
			D: condition #0
0281	40642	R/W	(Read/Write) select action channel for PTP
			R/W: ABCD (write to Flash RAM)
			A: condition #7
			B: condition #6
			C: condition #5
			D: condition #4
0282	40643	R/W	(Read/Write) select PTP rule enable/disable
			R/W: 0000 0000 xxxx xxxx(write to Flash RAM)
			X:0 disable
			X:1 enable
0283	40644	R/W	(Read/Write) Peer to Peer Enable/Disable
			R: 000A(hex)
			W: 000A(hex)
			A: 0:disable
			1:enable(write to Flash RAM)
0284	40645	R/W	(Read/Write) Mirror to(client) or Mirror
			from(server)
			R: 000A(hex)
			W: 000A(hex)
			A: 0: Mirror to(client)
			1: Mirror from(server)
			2: "Mirror to" and "Mirror from" simultaneously
			(write to Flash RAM)
0285	40646	R/W	(Read/Write) PTP IP address ip[0] low word for
			server
			R: AABB
			W: AABB
0286	40647	R/W	(Read/Write) PTP IP address ip[0] hi word for
			server
			R: AABB
			W: AABB
0287	40648	R/W	(Read/Write) PTP IP address ip[1] low word for
			server
			R: AABB
			W: AABB

0000	10510	D /777	(D. 1/11/1) DED TO 11 1 1/11/11 10
0288	40649	R/W	(Read/Write) PTP IP address ip[1] hi word for
			server
			R: AABB
			W: AABB
0289	40650	R/W	(Read/Write) PTP IP address ip[2] low word for
			server
			R: AABB
			W: AABB
028A	40651	R/W	(Read/Write) PTP IP address ip[2] hi word for
			server
			R: AABB
			W: AABB
028B	40652	R/W	(Read/Write) PTP IP address ip[3] low word for
			server
			R: AABB
			W: AABB
028C	40653	R/W	(Read/Write) PTP IP address ip[3] hi word for
			server
			R: AABB
			W: AABB
028D	40654	R/W	(Read/Write) PTP IP address ip[4] low word for
			server
			R: AABB
			W: AABB
028E	40655	R/W	(Read/Write) PTP IP address ip[4] hi word for
			server
			R: AABB
			W: AABB
028F	40656	R/W	(Read/Write) PTP IP address ip[4] low word for
			server
			R: AABB
			W: AABB
0290	40657	R/W	(Read/Write) PTP IP address ip[4] hi word for
			server
			R: AABB
			W: AABB
0291	40658	R/W	(Read/Write) PTP IP address ip[6] low word for
3-21			server
			551,61

			R: AABB
			W: AABB
0292	40659	R/W	(Read/Write) PTP IP address ip[6] hi word for
0272	1000		server
			R: AABB
			W: AABB
0293	40660	R/W	(Read/Write) PTP IP address ip[7] low word for
02)3	10000		server
			R: AABB
			W: AABB
0294	40661	R/W	(Read/Write) PTP IP address ip[7] hi word for
0251	10001		server
			R: AABB
			W: AABB
0295	40662	R/W	(Read/Write) PTP IP address ip[8] low word for
02/3	40002		client
			R: AABB
			W: AABB
0296	40663	R/W	(Read/Write) PTP IP address ip[8] hi word for
0290	40003	IX/ VV	client
			R: AABB
			W: AABB
0207	10661	D/W	
0297	40664	R/W	(Read) internal flags
			R: xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx
0200	10665	D	X:0 or 1
0298	40665	R	(Read) error code of peer to peer for client
			Error Code=0; OK
			Error Code=1; Requested Content Not Satisfiable
			Error Code=2; Remote Module Not Found
			Error Code=FFFF; Not Enabled
0299	40666	R	(Read) error code of peer to peer #1 for server
			Error Code=0; OK
			Error Code=1; Requested Content Not Satisfiable
			Error Code=2; Remote Module Not Found
			Error Code=FFFF; Not Enabled
029A	40667	R	(Read) error code of peer to peer #2 for server

			7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	
			Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	
029B	40668	R	(Read) error code of peer to peer #3 for server	
			Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	
029C	40669	R	(Read) error code of peer to peer #4 for server	
			Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	
029D	40670	R	(Read) error code of peer to peer #5 for server	
0272	10070		Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	
			21101 0000 11111,1100 2100100	
029E	40671	R	(Read) error code of peer to peer #6 for server	
			Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	
029F	40672	R	(Read) error code of peer to peer #7 for server	
			Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	
02A0	40673	R	(Read) error code of peer to peer #8 for server	
			Error Code=0; OK	
			Error Code=1; Requested Content Not Satisfiable	
			Error Code=2; Remote Module Not Found	
			Error Code=FFFF; Not Enabled	

	1	1	

Warning: When calibrating, you should calibrate the value for the selected output range by the certificated calibrator.

6 Appendix

6.1 SNMP MIB

An SNMP to I/O MIB file that can help you monitor I/O status with SNMP software. You can find the MIB file on the package.

(I). Public- System MIB:

Object ID	Description	Community,
(OID)		R/W Access
sysDescr	The sysDescr directive is used to define the system	Public,
	description of the host on which the SNMP agent	Read Only
	(server) is running. This description is used for the	
	sysDescr object instance of the MIB-II.	
	SYNTAX: DisplayString (SIZE (031))	
sysObjectID	The vendor's authoritative identification of the	Public,
	network management subsystem contained in the	Read Only
	entity. This value is allocated within the SMI	
	enterprises sub tree.	
	SYNTAX: DisplayString (SIZE (031))	
sysUpTime	The sysUpTime directive is used to measures the	Public,
	time, in hundredths of a second, since the last	Read Only
	system restart.	
	SYNTAX: DisplayString (SIZE (031))	
sysContact	The sysContact directive is used to define the system	Public,
	contact address used for the sysContact object	Read Only
	instance of the MIB-II.	
	SYNTAX: DisplayString (SIZE (031))	
sysName	The sysName directive is a string containing an	Public,
	administratively-assigned name for the system	Read Only
	running the SNMP agent. By convention, this should	
	be its fully-qualified domain name.	
	SYNTAX: DisplayString (SIZE (031))	
sysLocation	The sysLocation directive is used to define the	Public,
	location of the host on which the SNMP agent	Read Only
	(server) is running. This directive is used for the	-
	sysLocation object instance of the MIB-II.	
	SYNTAX: DisplayString (SIZE (031))	

(II).Private MIB - Intelligent I/O Server - 6520

Object ID (OID)	Description	Community, R/W Access
ch0-AO-Value	Analog Output signal (Channel 0)	Private,
	SYNTAX: <i>INTEGER</i> (065535)	Read/Write
ch1-AO-Value	Analog Output signal (Channel 1)	Private,
	SYNTAX: <i>INTEGER</i> (065535)	Read/Write
ch2-AO-Value	Analog Output signal (Channel 2)	Private,
	SYNTAX: INTEGER (065535)	Read/Write
ch3-AO-Value	Analog Output signal (Channel 3)	Private,
	SYNTAX: INTEGER (065535)	Read/Write

6.2 Analog Output (DA) Slew Rate and Calibration

(I) DA Slew Rate Code

DA Slew Rate Code				
Code	+10V	-10V∼+10V	20mA	
	V/sec	V/sec	mA/sec	
0000	Immediate Chan	ge		
0001	0.0625	0.125	0.125	
0010	0.125	0.25	0.25	
0011	0.25	0.5	0.5	
0100	0.5	1.0	1.0	
0101	1.0	2.0	2.0	
0110	2.0	4.0	4.0	
0111	4.0	8.0	8.0	
1000	8.0	16.0	16.0	
1001	16.0	32.0	32.0	
1010	32.0	64.0	64.0	
1011	64.0	128.0	128.0	

(I) D/A Calibration

Output	Zero Calibration	Span Calibration
Range(Hex)	Voltage/current	Voltage/current
0	1v	9v
1	-8v	8v
2	2mA	18mA

6.3 Revision History

Version	Description	Date
1.5	 Update "General" page Update "Data" page Update "Alarm" page Add "Logic Rules" page Add "Peer to Peer I/O" page Add "SNMP" page Add new Modbus/TCP Registers in FW208 	Sept. 1, 2009
1.4	Add configuration backup/restore Simply firmware upgrade procedure	Dec. 24, 2008
1.21	 Add description for Input Range of Current mode Change Pin No. table Change Device Finder Utility to Device Finder Popup Window due to Device Finder Utility is merged. Remove Web Configuration. Add steps and screen for Password Login and Change Add examples for Terminal mode Update Tag Properties popup window Add more description for firmware upgrading progress Change Watch dog timer to host watch dog timer in Register 40001/40002 Add description for Register 40038 to 40061 and 41000 to 41003. 	Jun. 25, 2008
1.2	Add Emulation mode operation description. Add Terminal mode operation description. Add description of the AI channels' floating state in "Data" page. Add Notes for the Simulation mode - OPC Server Utility Add Notes for Device IP change - Device Finder Utility Add Notes in How to upgrade firmware. Add description for Modbus/TCP address mapping. Add warning for calibration registers. Correct Wordings: Logic rule ->I/O rule, I/O module -> I/O server, pin table in appearance and description, Description of the SNMP and Trap.	Apr. 17, 2008
1.11	Update Modbus/TCP Reset Status register.	Feb. 20, 2008
1.1	Add Note for IP changed, Trap types, update latest datasheet info, correct some wordings, add calibration table.	Feb. 12, 2008
1.0	First Release Change V0.3 to V1.0	Jan. 30, 2008